

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|--|------------------|---------|------------------|
| S1 | 698 | (375/334).CCLS. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2006/10/01 14:36 |
| S2 | 121 | VCO near2 core | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 16:56 |
| S3 | 124 | switching near2 varactor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 14:37 |
| S4 | 2 | S2 and S3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 14:37 |
| S5 | 1245 | VCO WITH varactor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 15:16 |
| S6 | 725 | S5 and @pd<"20030815" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 16:57 |
| S7 | 144 | S6 and (VCO SAME parallel SAME varactor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:04 |
| S8 | 23 | S7 and (switch\$5 near4 varactor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 15:18 |

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| S9 | 838 | VCO same (varactor with diode) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 15:17 |
| S10 | 551 | S9 and @pd<"20030815" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 15:17 |
| S11 | 112 | S10 and (VCO SAME parallel SAME varactor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 15:17 |
| S12 | 17 | S11 and (switch\$5 near4 varactor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 15:18 |
| S13 | 1 | (US-5739730-\$).did. | USPAT | OR | ON | 2006/10/01 15:25 |
| S14 | 48 | (VCO near2 buffer) and varactor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 16:58 |
| S15 | 28 | S14 and @pd<"20030815" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 16:58 |
| S16 | 2 | S15 and (VCO SAME parallel SAME varactor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 16:58 |
| S17 | 15 | (VCO adj buffer) and varactor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 16:58 |

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| S18 | 10 | S17 and @pd<"20030815" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:04 |
| S19 | 2 | S18 and (VCO SAME parallel SAME varactor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 16:58 |
| S20 | 1 | (US-5650754-\$).did. | USPAT | OR | ON | 2006/10/01 17:03 |
| S21 | 125 | VCO WITH FSK | USPAT | OR | ON | 2006/10/01 17:04 |
| S22 | 111 | S21 and @pd<"20030815" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:39 |
| S23 | 2 | S22 and (VCO SAME parallel SAME varactor) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:04 |
| S24 | 1 | (US-6163568-\$).did. | USPAT | OR | ON | 2006/10/01 17:07 |
| S25 | 1 | (Frequency adj2 select\$3 adj2 unit)same input same mode same (frequency adj4 voltage) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:38 |
| S26 | 611 | S9 and ((control or select\$5) near3 voltage) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:39 |
| S27 | 402 | S26 and @pd<"20030815" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:41 |

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| S28 | 190 | S27 and (input and mode and voltage) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:42 |
| S29 | 29 | S27 and (input with mode with voltage) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/01 17:43 |
| S30 | 1 | (US-3962640-\$).did. | USPAT | OR | ON | 2006/10/01 17:50 |

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Broomfield, C.; Yiming You; Parsons, R.; Everard, J.;
Frequency Control Symposium and PDA Exhibition Jointly with the 17th Euro
and Time Forum, 2003. Proceedings of the 2003 IEEE International
4-8 May 2003 Page(s):417 - 422
Digital Object Identifier 10.1109/FREQ.2003.1275128
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2. **A CAD design of narrow band dielectric resonator voltage controlled osc**
in phased-lock DROs
Biglarbegian, B.; Hodjat Kashani, F.; Arianpoo, N.;
Microwave & Telecommunication Technology, 2005 15th International Crimean
Volume 1, 12-16 Oct. 2005 Page(s):158 - 160 Vol. 1
Digital Object Identifier 10.1109/CRMICO.2005.1564850
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IEEE JNL IEEE Journal or Magazine

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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

1. **A quenchable GaAs HBT X-band VCO for switched band synthesizer arch**
 Kobayashi, K.W.; Smith, D.M.; Kau, C.P.; Oki, A.K.; Sharma, A.K.; Allen, B.R.;
Microwave and Millimeter-Wave Monolithic Circuits Symposium, 1996. Digest,
 1996

16-18 June 1996 Page(s):107 - 110

Digital Object Identifier 10.1109/MCS.1996.506314

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2. **A fully silicon monolithic integrated 868/915 MHz FSK/FM/ASK transmitt**
 Laute, A.; Peter, J.;
Silicon Monolithic Integrated Circuits in RF Systems, 2000. Digest of Papers. 2
 Meeting on

26-28 April 2000 Page(s):38 - 42

Digital Object Identifier 10.1109/SMIC.2000.844293

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3. **A fully integrated PLL frequency synthesizer LSI for mobile communicati**
 Yasunaga, T.; Hirano, S.; Maeda, R.; Hiraoka, Y.; Andou, T.; Miyahara, Y.;
Radio Frequency Integrated Circuits (RFIC) Symposium, 2001. Digest of Paper,
 20-22 May 2001 Page(s):65 - 68

Digital Object Identifier 10.1109/RFIC.2001.935643

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4. **Enhancing the simulation accuracy of RF designs with consistent charac**
 modeling techniques

 Cojocaru, V.; Markell, D.; Capwell, J.; Weller, T.; Dunleavy, L.;
ARFTG Conference Digest, Spring 2002. 59th

7 June 2002 Page(s):7 pp.

Digital Object Identifier 10.1109/ARFTGS.2002.1214693

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